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		Application Number	10/720,297			
TRANSMITTAL		Filing Date	November	24, 2003		
FORM		First Named Inventor	JOE A. OF	RTIZ ET A	L.	
		Art Unit	2838			
	5e.)	Examiner Name	Patel, Rajn	nikant		
(to be used for all correspondence after initial Total Number of Pages in This Submission	niing)	Attorney Docket Number	PD-03W08	31		
	ENCI	LOSURES (Check a	II that apply)		
Fee Transmittal Form Fee Attached Amendment/Reply After Final Affidavits/declaration(s) Extension of Time Request Express Abandonment Request Information Disclosure Statement		Drawing(s) Licensing-related Papers Petition Petition to Convert to a Provisional Application Power of Attorney, Revocat Change of Correspondence Terminal Disclaimer Request for Refund CD, Number of CD(s) Landscape Table on C	Address		Appea of App Appea (Appea Proprie	I Communication to Board eals and Interferences I Communication to TC I Communication to TC I Notice, Brief, Reply Brief) etary Information Letter Enclosure(s) (please Identify it
Certified Copy of Priority Document(s) Reply to Missing Parts/ Incomplete Application Reply to Missing Parts under 37 CFR 1.52 or 1.53	Remar	rks				
SIGNA	TUREC	OF APPLICANT, ATT	ORNEY C	DR AGE	FNT	
Firm Name RAYTHEON COMPANY Signature Printed name JOHN E. GUNTHER	Firm Name RAYTHEON COMPANY Signature					
Date August 17, 2005	Date Peg No.					
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

JOE A. ORTIZ ET AL.

Serial No.: 10/720,297

Filed: November 24, 2003

Title: METHOD FOR INPUT CURRENT

) Date: August 17, 2005
) Art Unit: 2838
)

Examiner: Patel, Rajnikant

REGULATION AND ACTIVE POWER FILTER WITH INPUT VOLTAGE...

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents Post Office Box 1450 Alexandria, VA 22313

Dear Sir:

The references cited on the enclosed form PTO/SB/08A/08B (copies of references enclosed) may be material to the examination of the above-identified patent application and are, therefore, submitted in compliance with the duty of disclosure defined in 37CFR 1.56.

This Information Disclosure Statement is not to be construed as a representation that a search has recently been made, or that additional information constitutes prior art.

I hereby certify that each item in the Search Report was cited by a foreign patent office, in a counterpart foreign application, not more than three (3) months prior to the filing of this Statement.

Respectfully submitted,

JOE A. ORTIZ ET AL

Raytheon Company Bldg. EO4, Mail Station N119 P.O. Box 902 El Segundo, CA 90245-0902 John E. Gunther

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PTO/SB/08A (08-03)

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(Use as many sheets as necessary)

Sheet	1	of	2

Complete if Known			
Application Number	10/720,297		
Filing Date	November 24, 2003		
First Named Inventor	Joe A. Ortiz		
Art Unit	2838		
Examiner Name	Patel, Rajnikant		
Attorney Docket Number	PD-03W081		

			U. S. PATEN	DOCUMENTS	
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ^{2 (# known)}	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		^{US-} 4,891,744 A	1/2/1990	Yamamoto	C1 L27 - L63
		US-			C3 L42 - C4 L13
		U\$-			FIGS 1, 2, 4
		US-			
		^{US-} 4,872,127 A	10/3/1989	Nolan	C1 L6 - L15
		US-			
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	FOREIGN PATENT DOCUMENTS					
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Substitute for form 1449/PTO	Complete if Known			
	Application Number	10/720,297		
INFORMATION DISCLOSURE	Filing Date	11/24/2003		
STATEMENT BY APPLICANT	First Named Inventor	Joe A. Ortiz		
(Use as many sheets as necessary)	Art Unit	2838		
(USC as many sneets as necessary)	Examiner Name	Patel, Rajnikant		
Sheet 2 of 2	Attorney Docket Number	PD-03W-81		

		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		RAMI, NAIM ET AL: "H Deg Deg Control of Boost Converters: Comparison to Voltage Mode, feedforward, and Current Mode Controls",	
		Record of the Annual Power Electronics Specialists Conference, 12 June 1995, pp 1327-1332, Vol. 2 conf. 26, IEEE, New York, NY, USA	
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		15 February 1998, pp 170 - 176, Vol. 1, IEEE, New York, NY, USA	
		KANEMARU, S ET AL: "Analysis and Optimum Design of a Buck-Type DC-to DC Converter Employing Load current Feedforward", Power Electronics	
		Specialists Conference, 17 May 1998, pp 309-314, Vol. 1, IEEE, New York, NY, USA	

Examiner	Date	
Signature	Considered	

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